TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

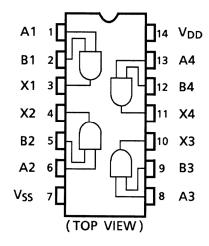
TC4081BP,TC4081BF,TC4081BFN

TC4081B Quad 2-Input AND Gate

TC4081B is positive logic AND gates with two inputs respectively.

Since all the outputs of these gates are equipped with the buffer circuits of inverters, the input/output propagation characteristic has been improved and variation of propagation time caused by increase of load capacity is kept minimum.

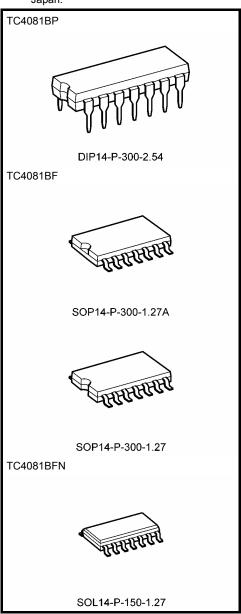
Pin Assignment



Logic Diagram 1/4 TC4081B



Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

 DIP14-P-300-2.54
 : 0.96 g (typ.)

 SOP14-P-300-1.27A
 : 0.18 g (typ.)

 SOP14-P-300-1.27
 : 0.18 g (typ.)

 SOL14-P-150-1.27
 : 0.12 g (typ.)



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD}	V _{SS} - 0.5~V _{SS} + 20	V
Input voltage	V _{IN}	V _{SS} - 0.5~V _{DD} + 0.5	V
Output voltage	V _{OUT}	V _{SS} – 0.5~V _{DD} + 0.5	V
DC input current	I _{IN}	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T _{ope}	-40~85	°C
Storage temperature range	T _{stg}	−65 ~ 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Recommended Operating Conditions (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V_{DD}	_	3	_	18	V
Input voltage	V _{IN}	_	0	_	V _{DD}	V

Note: The recommended operating conditions are required to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

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Static Electrical Characteristics ($V_{SS} = 0 V$)

		Sym-	Test Condition		-40°C			25°C			85°C	
Charac	teristics	bol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
High-level voltage	output	V _{OH}	$ I_{OUT} < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	5 10	4.95 9.95	_ _	4.95 9.95	5.00	_ _	4.95 9.95	_ _	V
				15	14.95		14.95	15.00		14.95	_	
Low-level	output	V _{OL}	I _{OUT} < 1 μA	5 10		0.05 0.05	_	0.00	0.05 0.05	_	0.05 0.05	V
voltage		VOL	$V_{IN} = V_{SS}, V_{DD}$	15	_	0.05	_	0.00	0.05	_	0.05	v
			V _{OH} = 4.6 V	5	-0.61	_	-0.51	-1.0	_	-0.42	_	
			V _{OH} = 2.5 V	5	-2.50	_	-2.10	-4.0	_	-1.70	_	mA
Output hig	h current	I _{OH}	V _{OH} = 9.5 V	10	-1.50	_	-1.30	-2.2	_	-1.10	_	
			V _{OH} = 13.5 V	15	-4.00	_	-3.40	-9.0	_	-2.80	_	
			$V_{IN} = V_{DD}$									
		l _{OL}	V _{OL} = 0.4 V	5	0.61	_	0.51	1.2	_	0.42	_	mA
Output lov	, ourront		V _{OL} = 0.5 V	10	1.50	_	1.30	3.2	_	1.10	_	
Output lov	Current		V _{OL} = 1.5 V	15	4.00	_	3.40	12.0	_	2.80	_	
			$V_{IN} = V_{SS}, V_{DD}$									
		V _{IH}	V _{OUT} = 0.5 V, 4.5 V	5	3.5	_	3.5	2.75	_	3.5	_	\/
Input high	voltago		V _{OUT} = 1.0 V, 9.0 V	10	7.0	_	7.0	5.50	_	7.0	_	
input nign	voitage		V _{OUT} = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.0	_	V
			$ I_{OUT} < 1 \mu A$									
			V _{OUT} = 0.5 V, 4.5 V	5	_	1.5	_	2.25	1.5	_	1.5	
Input low y	voltago	.,	V _{OUT} = 1.0 V, 9.0 V	10	_	3.0	_	4.50	3.0	_	3.0	V
Input low voltage		V _{IL}	V _{OUT} = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	V
			$ I_{OUT} < 1 \mu A$									
Input	"H" level	I _{IH}	V _{IH} = 18 V	18	_	0.1	_	10 ⁻⁵	0.1	_	1.0	μА
current	"L" level	IJL	V _{IL} = 0 V	18	_	-0.1	_	-10 ⁻⁵	-0.1	_	-1.0	μΑ
			\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	5	_	0.25	_	0.001	0.25	_	7.5	_
Quiescent supply current		I_{DD}	$V_{IN} = V_{SS}, V_{DD}$ (Note)	10	_	0.50	_	0.001	0.50	_	15.0	μΑ
			(14016)	15	_	1.00	_	0.002	1.00	_	30.0	

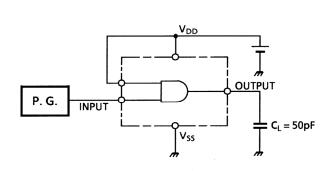
Note: All valid input combinations.

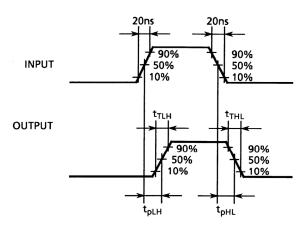
Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Characteristics	Symbol		V _{DD} (V)	IVIIII	τyp.	IVIAX	Offic
Output transition time			5	_	70	200	
Output transition time	t _{TLH}	_	10	_	35	100	ns
(low to high)			15	_	30	80	
Output transition time			5	_	70	200	
Output transition time (high to low)	t _{THL}	_	10	_	35	100	ns
(flight to low)			15	_	30	80	
	t _p LH		5	_	65	200	ns
Propagation delay time		_	10	_	30	100	
			15	_	25	80	
	t _{pHL}	_	5	_	65	200	
Propagation delay time			10	_	30	100	ns
			15	_	25	80	
Input capacitance	C _{IN}	_		5	7.5	pF	

Circuit and Waveform for Measurement of Dynamic Characteristics

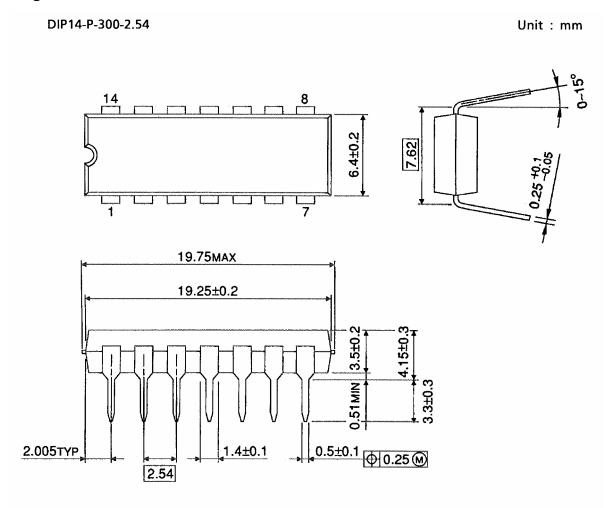
Circuit Waveform







Package Dimensions

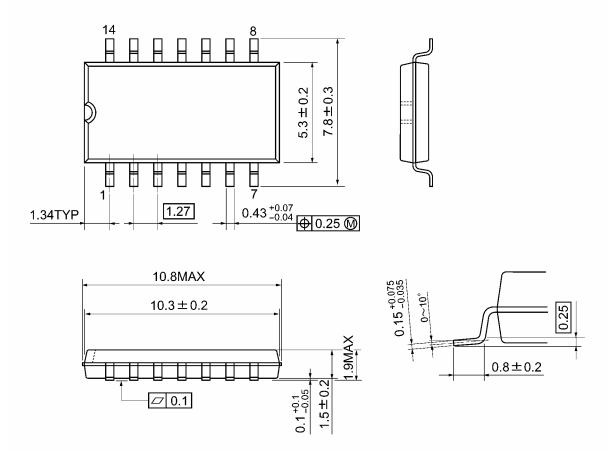


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Weight: 0.96 g (typ.)

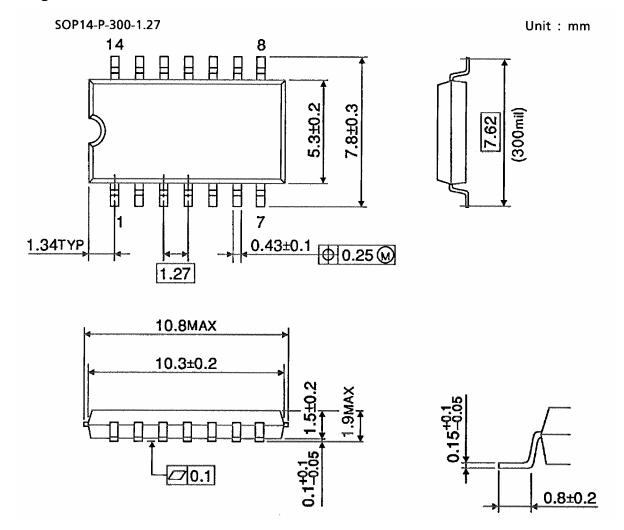
Package Dimensions

SOP14-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

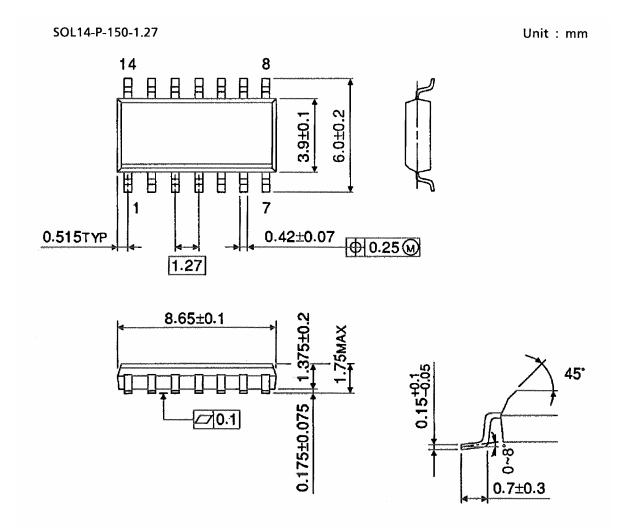
Package Dimensions



Weight: 0.18 g (typ.)



Package Dimensions (Note)



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Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

Note: Lead (Pb)-Free Packages

DIP14-P-300-2.54 SOP14-P-300-1.27A SOL14-P-150-1.27

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